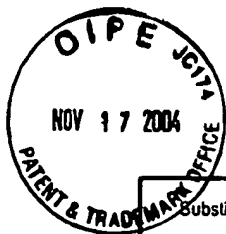




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PTO/SB/08a/b (08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet	1	of	1	Application Number	09/821,444-Conf. #1745
				Filing Date	March 29, 2001
				First Named Inventor	Junji Sakai
				Art Unit	2183 2191
				Examiner Name	Not Yet Assigned
				Attorney Docket Number	H2041.0058

Complete if Known

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			

FOREIGN PATENT DOCUMENTS						
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MA	CA	JUNJI SAKAI, ET AL., "Automatic Parallelizing Method for Control-Parallel Multi-Threaded Architecture," Symposium Series for the Information Processing Society of Japan, Collection of Papers of JSPP '98, Parallel Process Symposium, Vol. 98, No. 7, June 1998, pp. 383-390	
MA	CB	Japanese Office Action issued August 24, 2004 (w/ translation of relevant portion)	

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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

Examiner Signature	<i>M. S. Sutton</i>	Date Considered	8-29-05
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